
Skip SG BCH4 32B Spare User Manual

General Description and Name

This BBM is very similar with BBM ‘Skip BB With BBT With BCH4 Ecc’, but with configurable OOB size for calculate sub-page ECC.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn’t exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = “Skip SG BCH4 32B Spare”

Spare area : Please refer to “Description of common NAND special features.pdf”. *Normally set as “ECC ” for this BBM.* [Default ‘Disabled’]

Skip SG BCH4 sub page spare size = “32”

Normally the valid range is from 16 to 32 bytes. For this case, value could be 16, 25, 27 or 32. Default “32”

Skip SG BCH4: do second swap32? = “Yes” or “No”

Default “No”.

Normally set ‘No’ for this BBM. If “yes”, swap shown as below:

0x01 0x02 0x03 0x04 => 0x04 0x03 0x02 0x01

BB: max allowed = The Max permitted number of bad blocks. Please note, if the actual bad block number of the chip is bigger than this value, the algorithm error will be reported. [Default “FFFFFFFF”, not change; or equal 2% of total blocks]

Special Notes

Revision History

- V1.0 24th Jan. 2017
Create this spec.
- V1.1 14th Feb. 2017
Add new special feature.
- V1.2 20th Feb. 2017
Add new special feature 'BB:max allowed'

Appendix

You can get the file "Description of common NAND special features.pdf" from <http://ftp.dataio.com/FCNotes/BBM/>